

1/20

FIG. 1

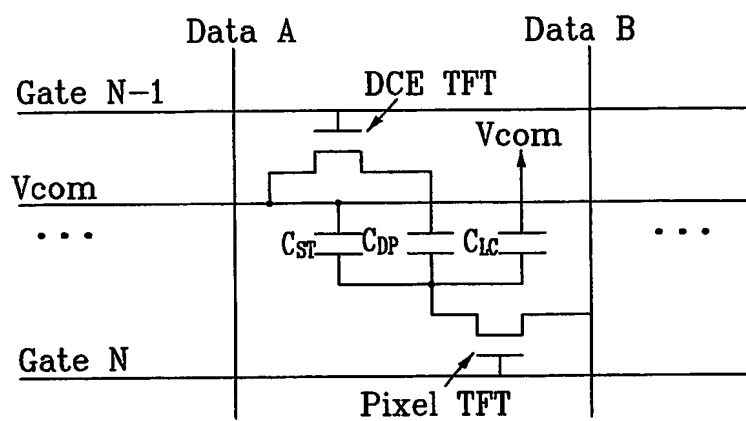
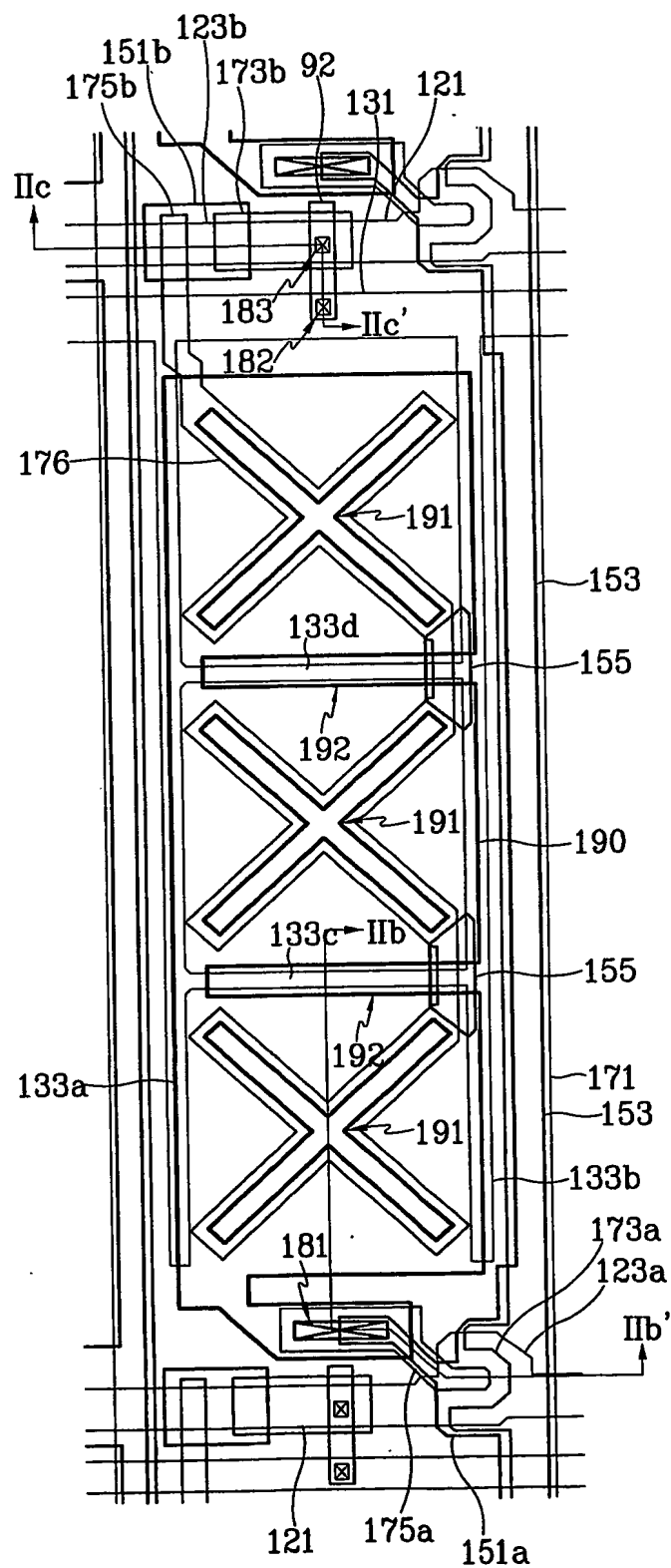


FIG. 2A



3/20

FIG. 2B

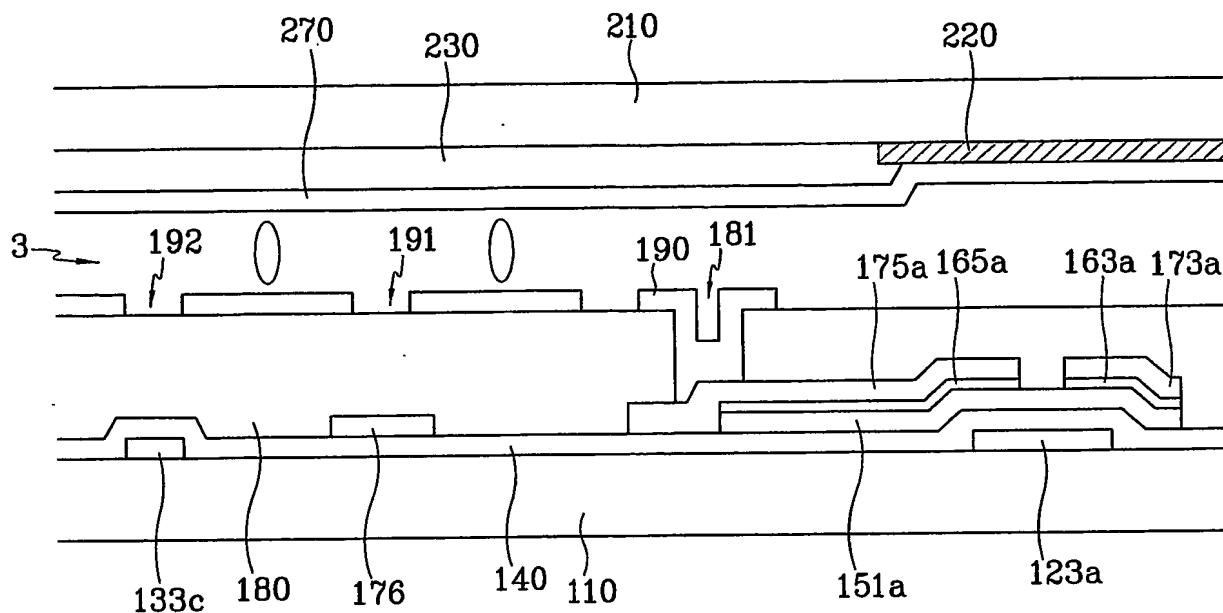


FIG. 2C

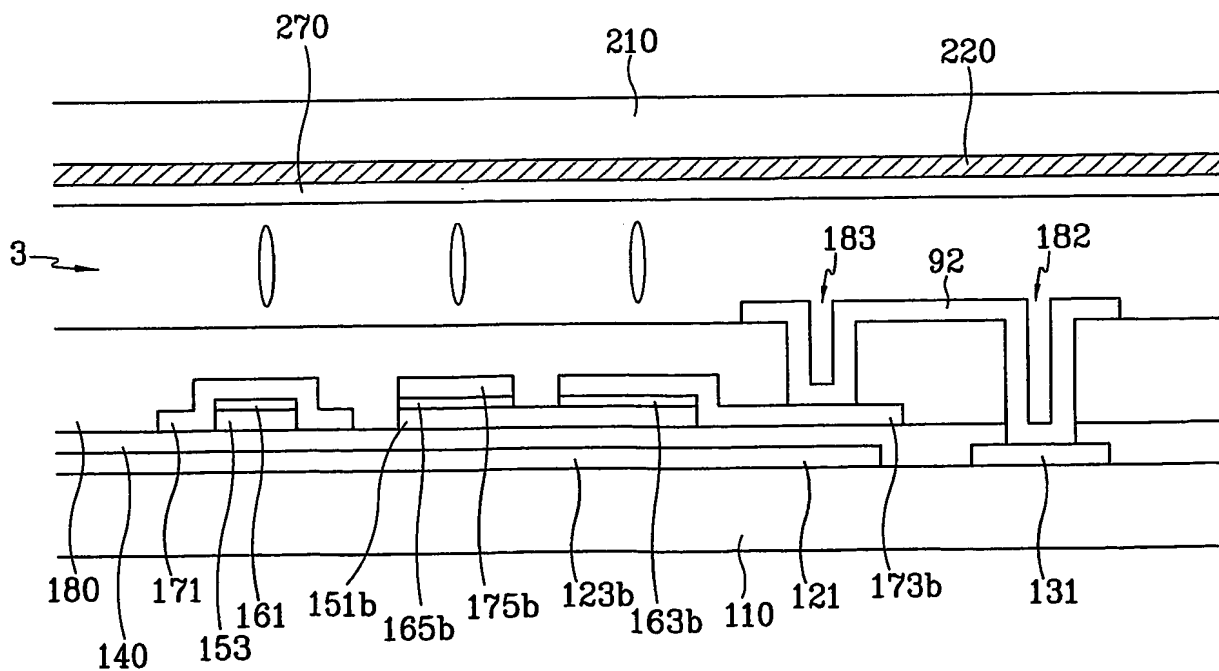


FIG. 3A

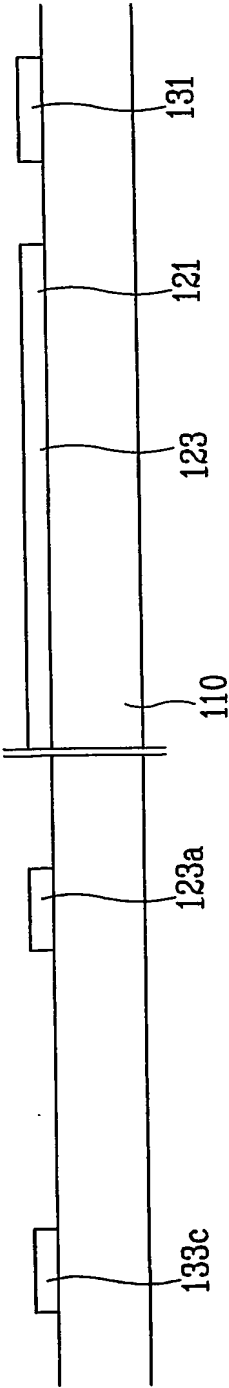


FIG.3B

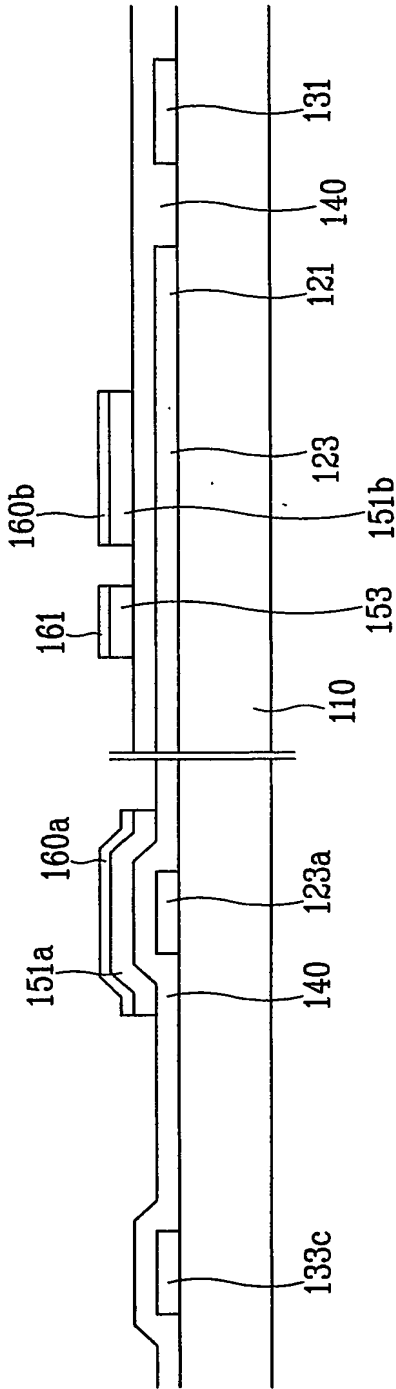




FIG. 3D

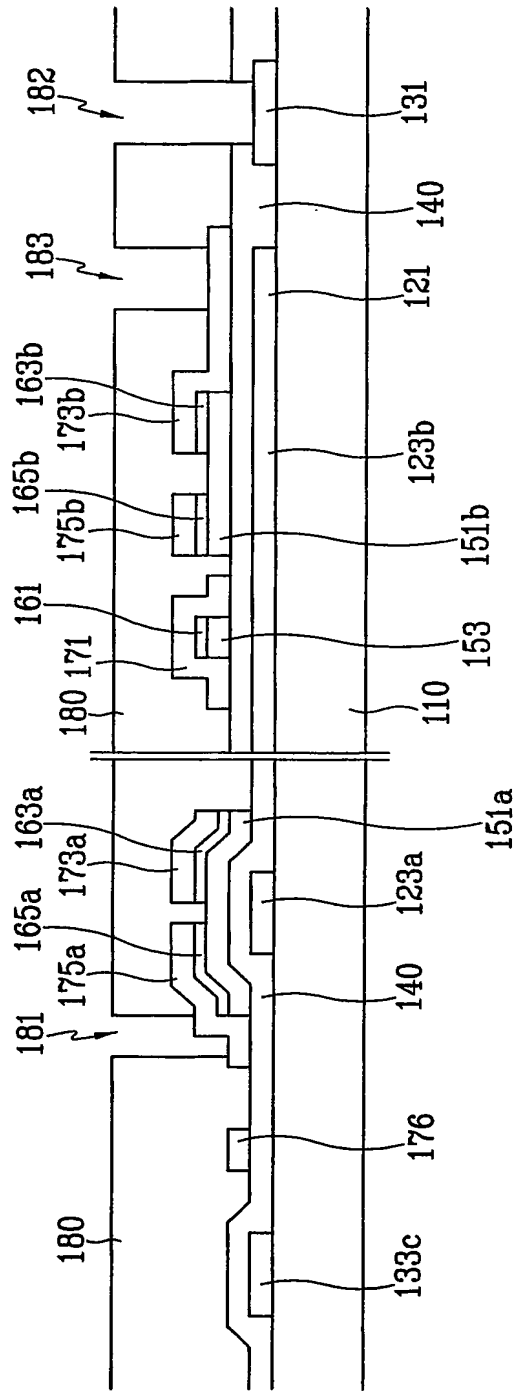


FIG. 4

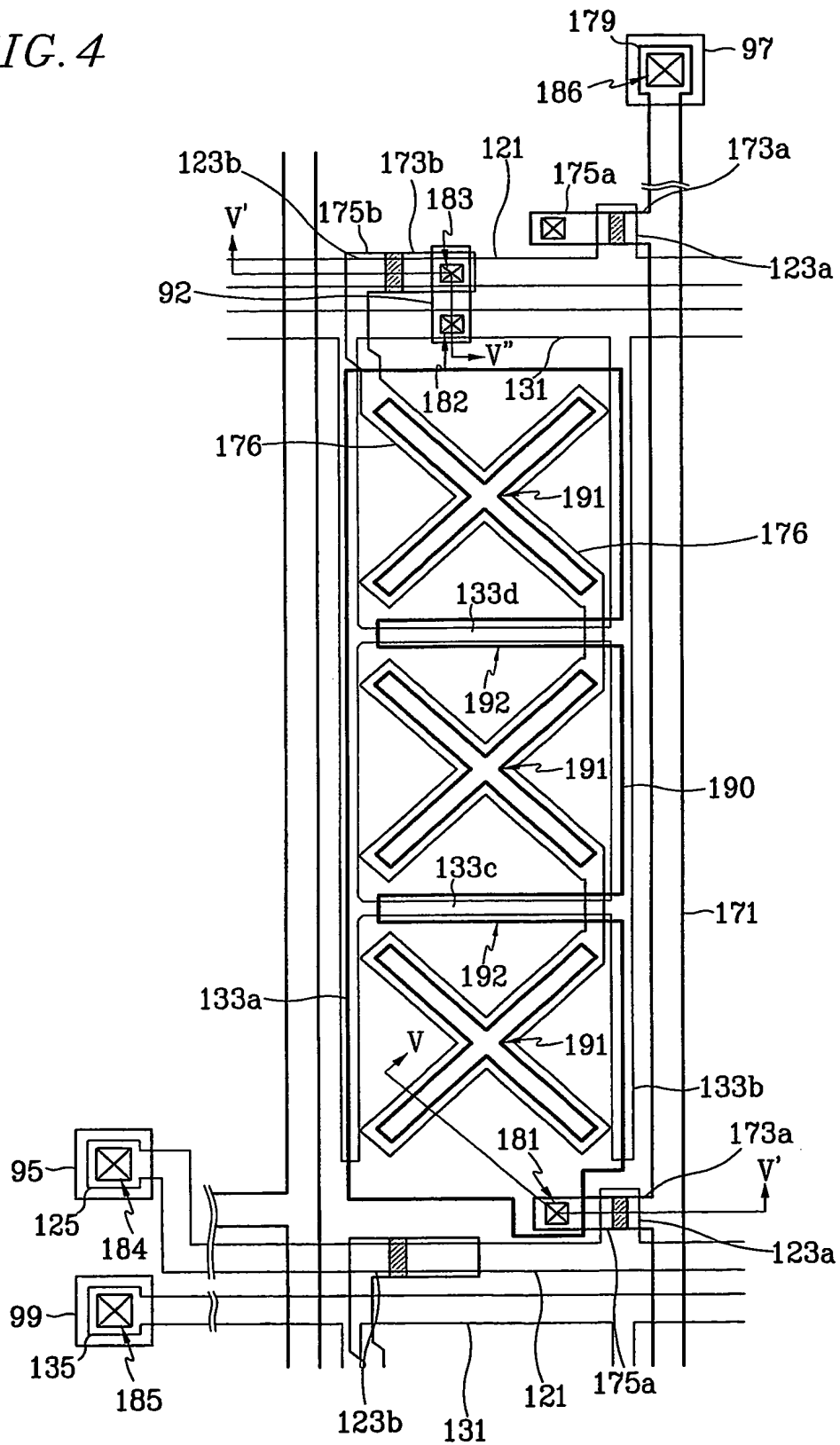
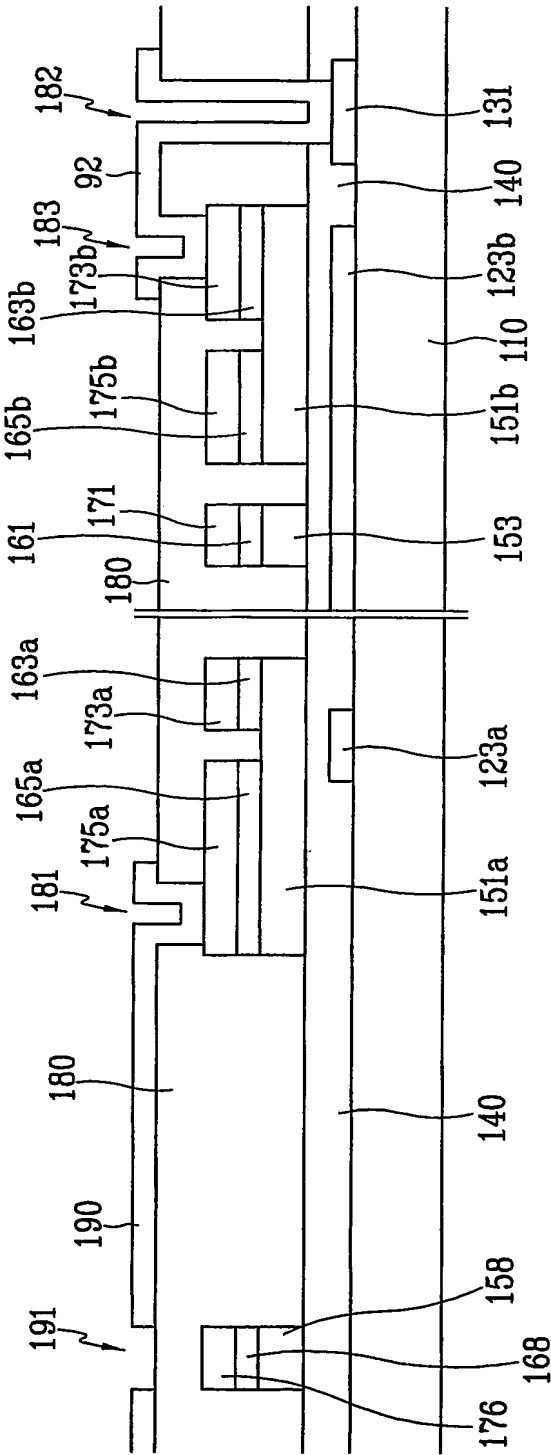




FIG. 5



10/20

FIG. 6A

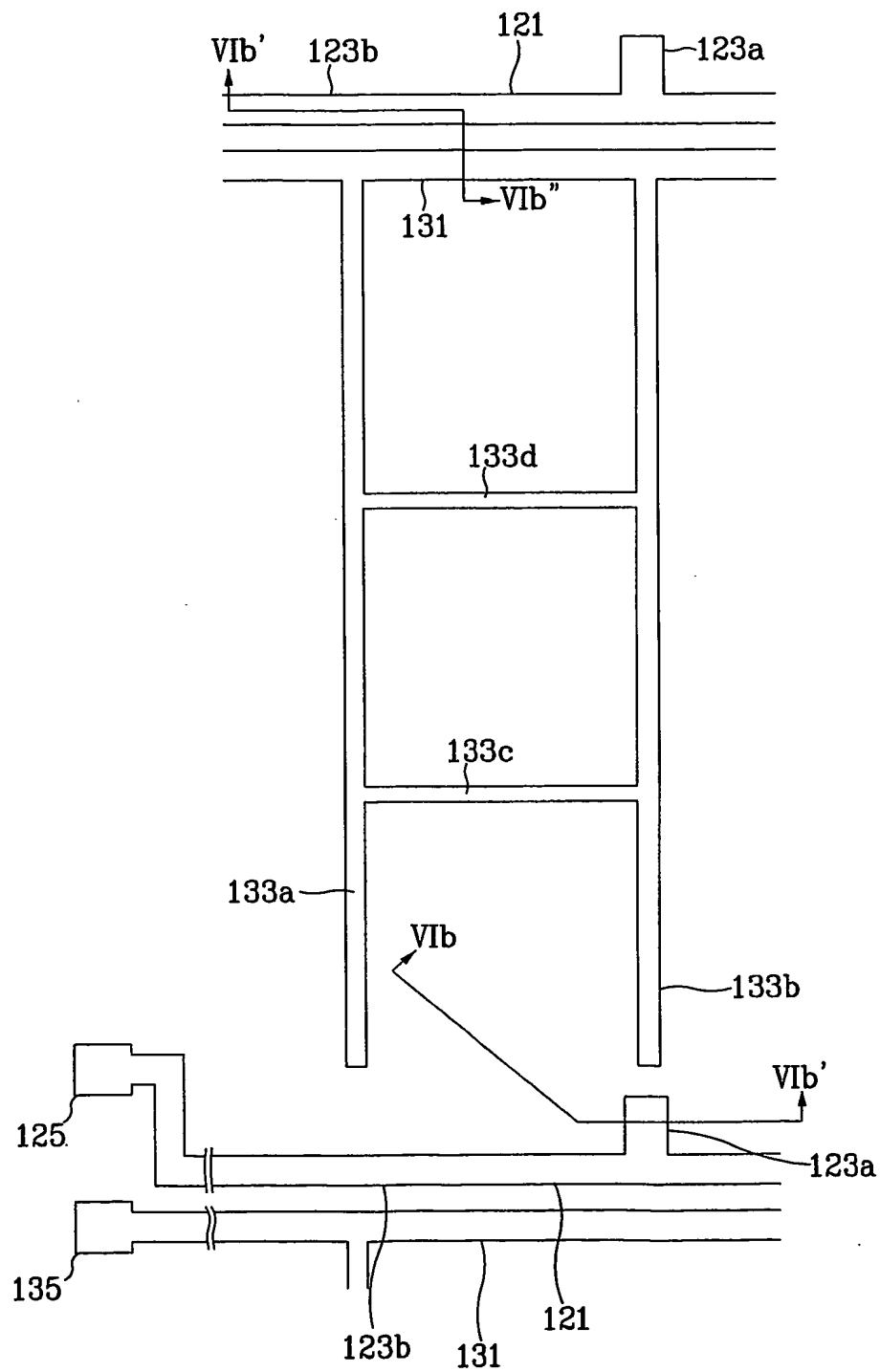


FIG. 6B

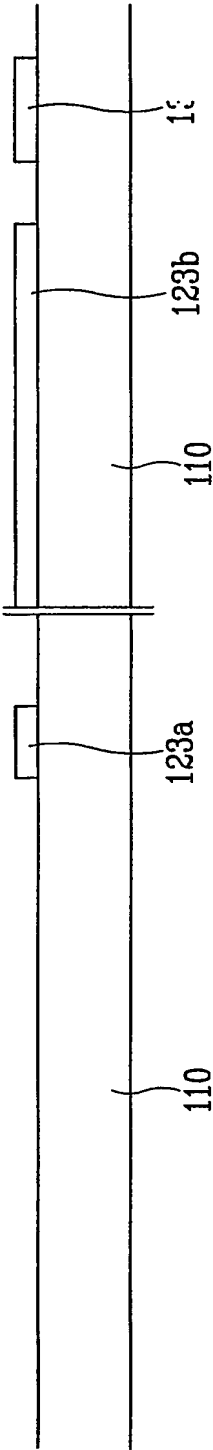
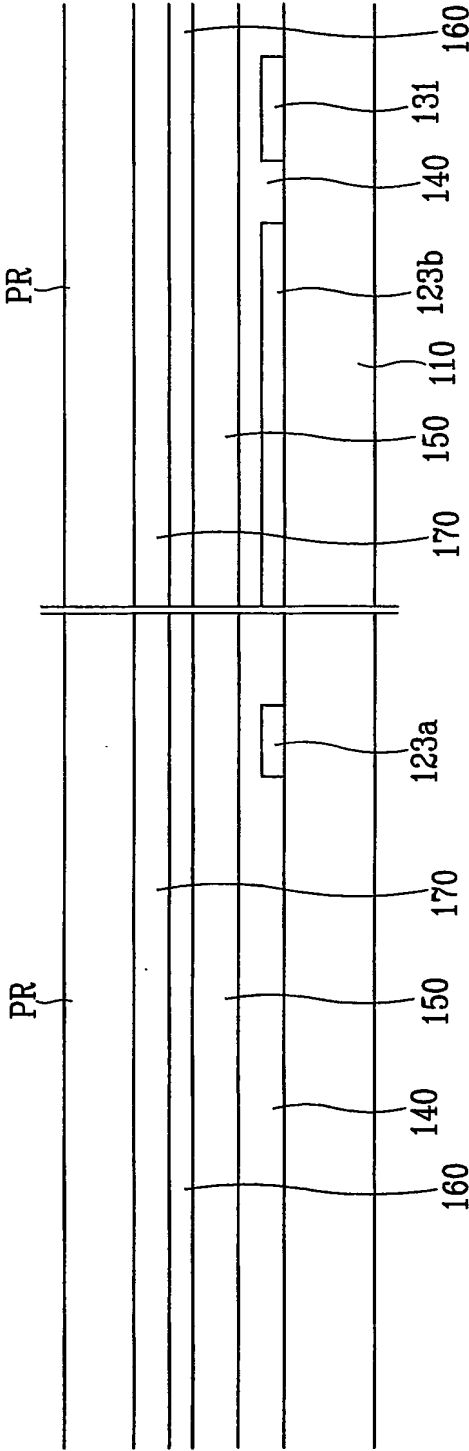


FIG. 7



13/20

FIG. 8A

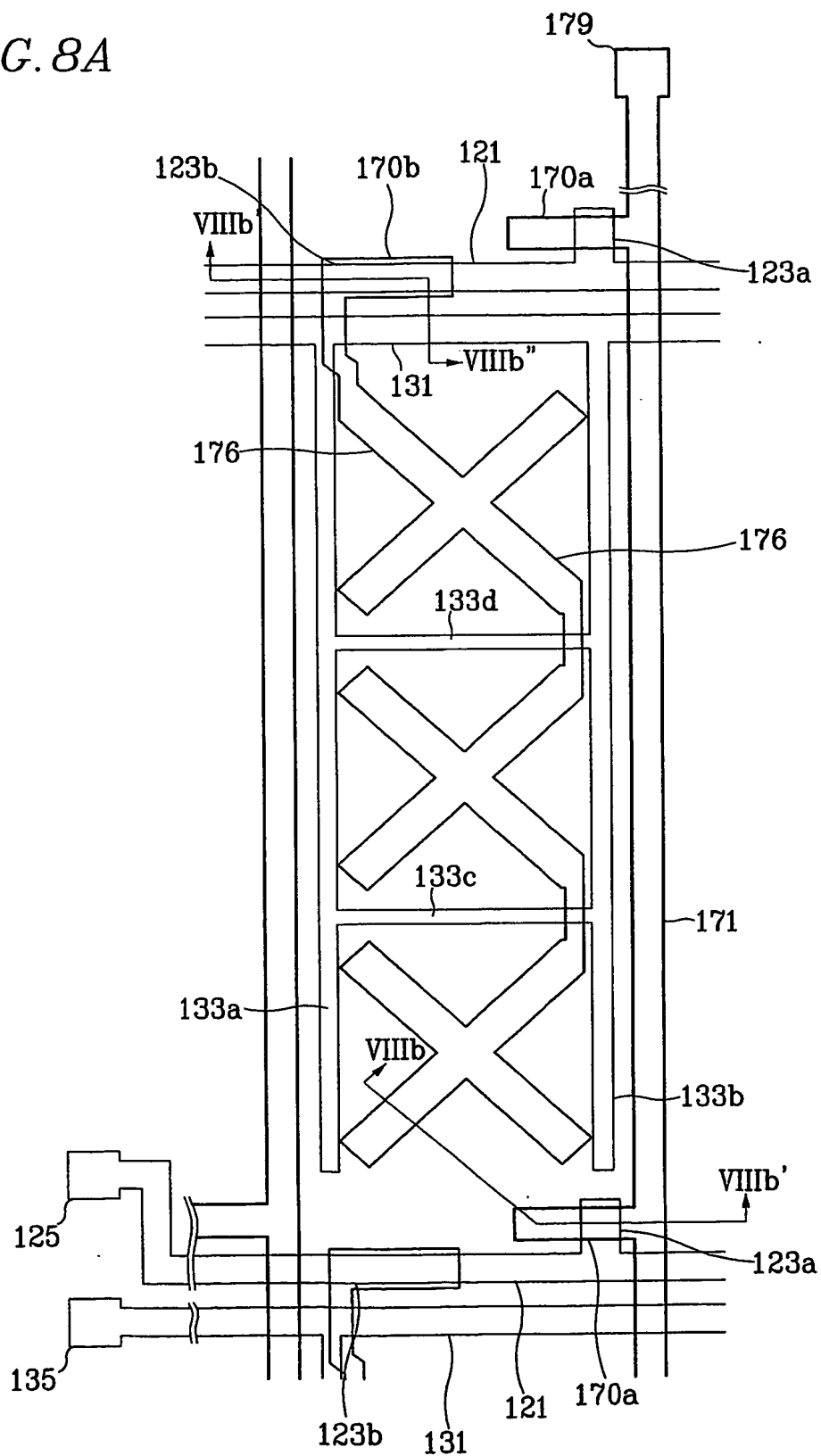


FIG. 8B

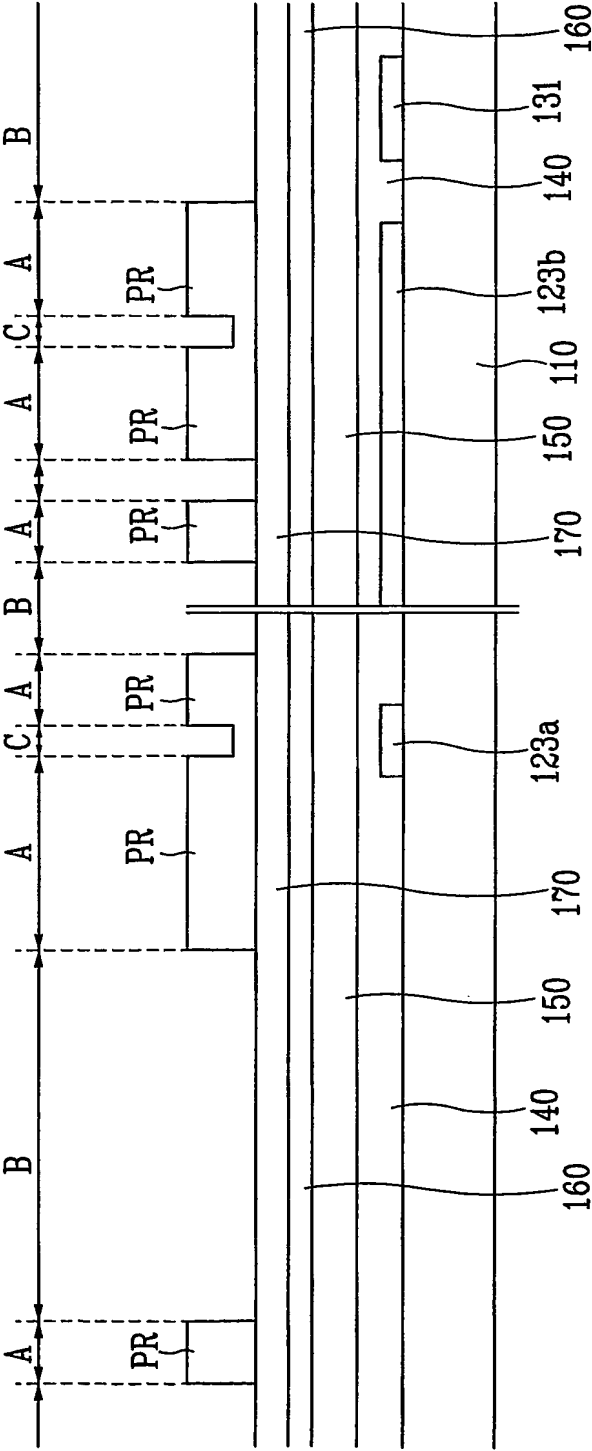


FIG. 9

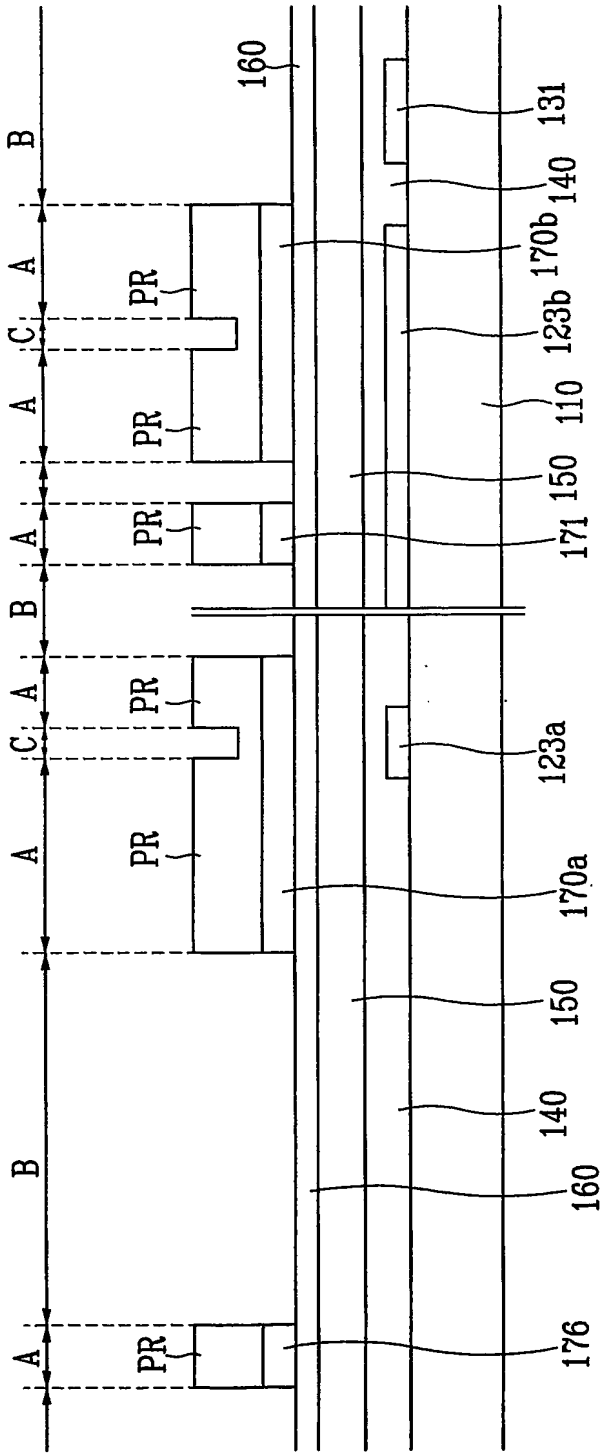
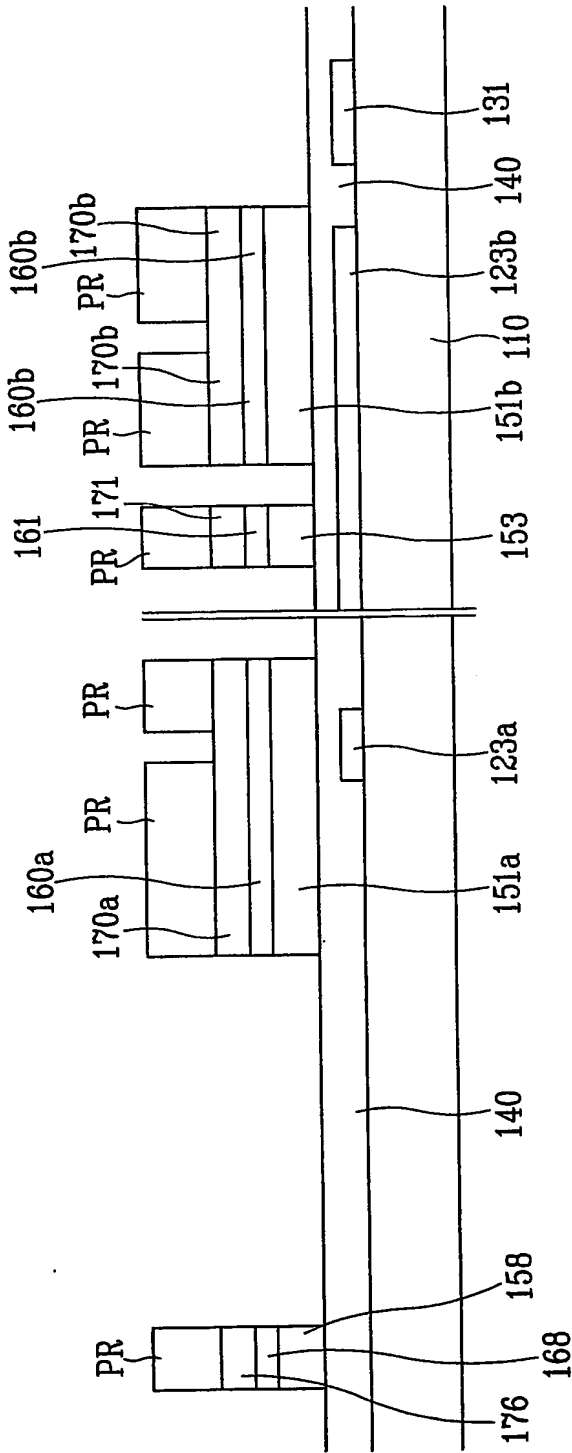


FIG. 10





17/20

FIG. 11A

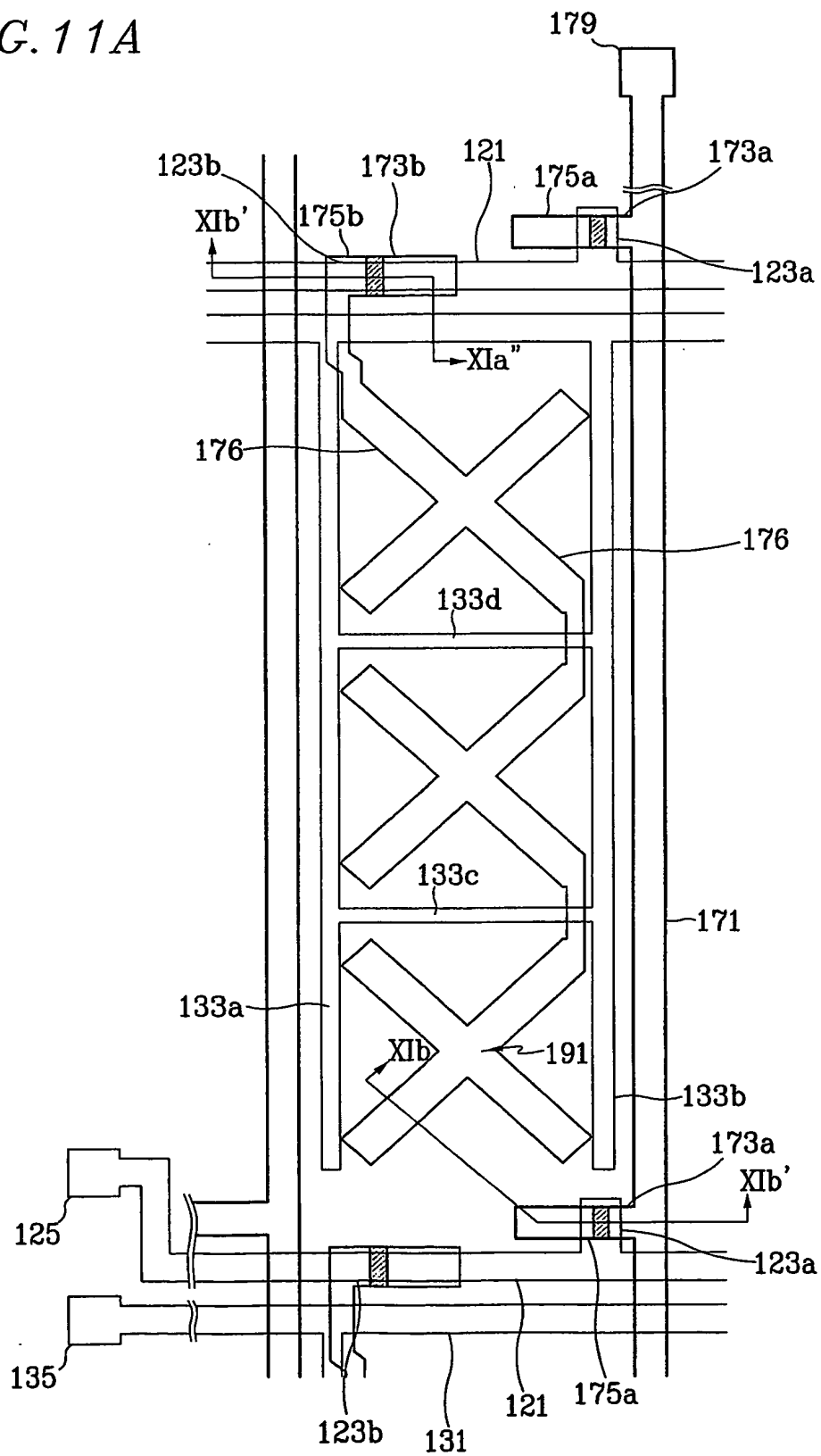


FIG. 11B

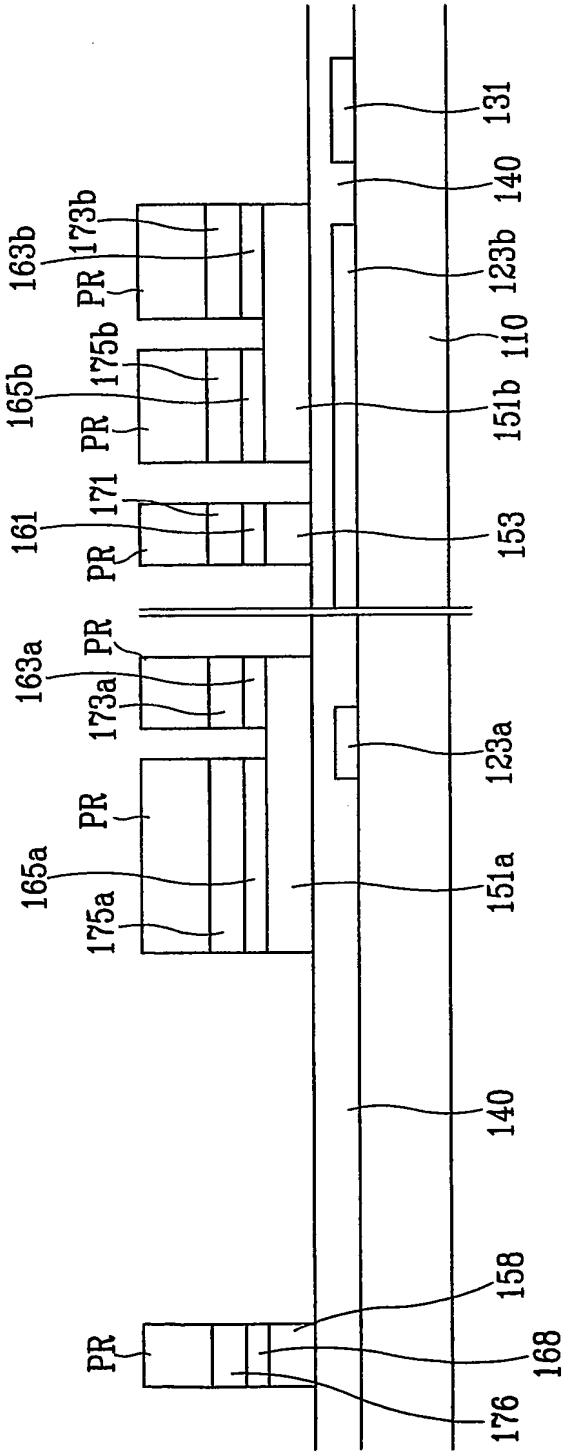
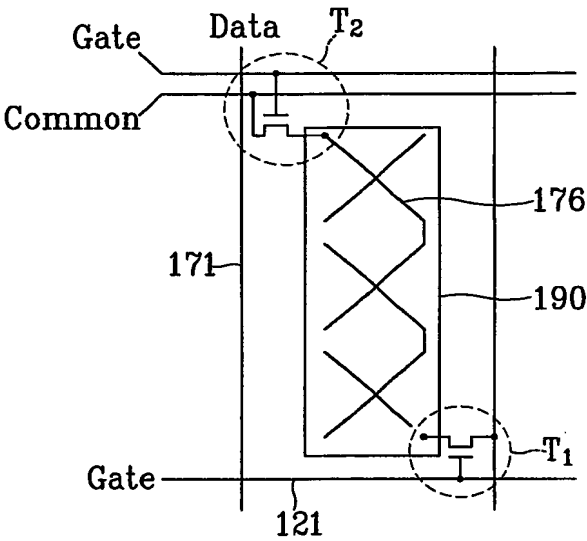
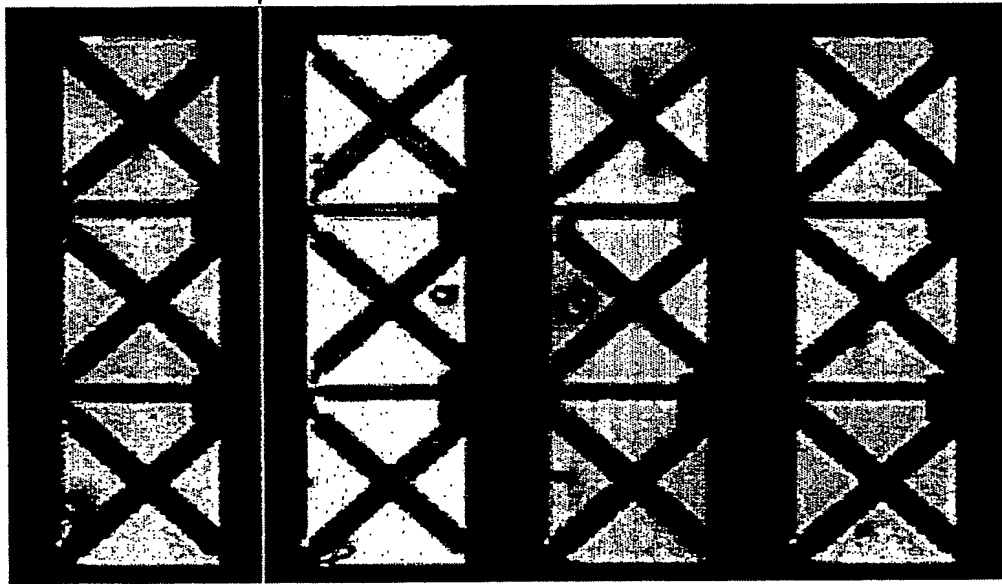


FIG. 12



20/20

*FIG.13*



<Vcom DCE VA Mode>

<Cross DCE VA Mode>